501.40716CX1

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

O. Inoue, et al.

Application No.:

**TBD** 

Filed:

January 9, 2004

For:

FABRICATION METHOD OF SEMICONDUCTOR

INTEGRATED CIRCUIT DEVICE

Expected Group:

1756

Expected Examiner: C. Young

# INFORMATION DISCLOSURE STATEMENT UNDER 37 CFR § 1.97 AND § 1.98

Mail Stop Patent Application Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 January 9, 2004

Sir:

Pursuant to Applicants' duty of disclosure, enclosed please find a List, on a form substantially equivalent to Form PTO-1449, of documents cited in connection with a prior application of the above-identified application, Application No. 09/964,341, filed September 28, 2001, and of a document enclosed herewith and discussed on page 1 of the specification of the above-identified application.

Since Application No. 09/964,341 is being relied upon under 35 USC § 120 in the above-identified application, copies of the documents on the enclosed List, other than Japanese Patent Document No. 11-135402 (and English translation thereof) are not enclosed herewith. See 37 CFR § 1.98(d).

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This Information Disclosure Statement is being submitted concurrently with

the filing of the above-identified Continuation application. Accordingly, requirements

of 37 CFR §1.97(b) are clearly satisfied.

Requirements of 37 CFR § 1.98(a)(3) are clearly satisfied, with respect to

No. 11-135402, by discussion of this document on page 1 of Applicants'

specification and by the submitted English translation.

In view of all of the foregoing, it is respectfully submitted that all applicable

requirements of 37 CFR § 1.97 and § 1.98 have been satisfied, in connection with

each of the documents on the enclosed List. Accordingly, consideration of the listed

documents, upon examination of the above-identified application, is respectfully

requested.

Please charge any shortage in fees due in connection with the filing of this

paper to the Deposit Account No. 01-2135 (Case No. 501.40716CX1), and please

credit any excess fees to such Deposit Account.

Respectfully submitted,

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Form PTO-1449	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DKT. NO.	APPLICATION NO.		
		501.40716CX1	TBD		
INFORMAT	ION DISCLOSURE STATEMENT BY APPLICANT	APPLICANT O. Inoue, et al.			
(Use s	everal sheets if necessary)	FILING DATE	EXPECTED GROUP		
`		January 9, 2004	1756		

#### **U.S. PATENT DOCUMENTS**

Examin Initial	er	Document Number	Date	Name	Class	Subclass	Filing Date
	AA	6,482,555	11/19/2002	Chen, et al.			
	AB	6,335,130	1/1/2002	Chen, et al.			
	AC						
	AD						

### **FOREIGN PATENT DOCUMENTS**

Examiner Initial		Document Number	Date	Country	Class	Subclass	Translation/ Abstracts	
							Yes	No
	AE	56-30129	3/26/1981	Japan			Х	
	AF	59-22050	2/4/1984	Japan			Х	
	AG	11-135402	5/21/1999	Japan			Х	
	AH							1
	Al							
	AJ							
	AK							
	AL							

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AM		
AN	 	
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AP		
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AR	·	

## Examiner Date Considered

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<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>&</sup>lt;sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at <a href="www.uspto.gov">www.uspto.gov</a> or MPEP 901.04. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO Standard St.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.